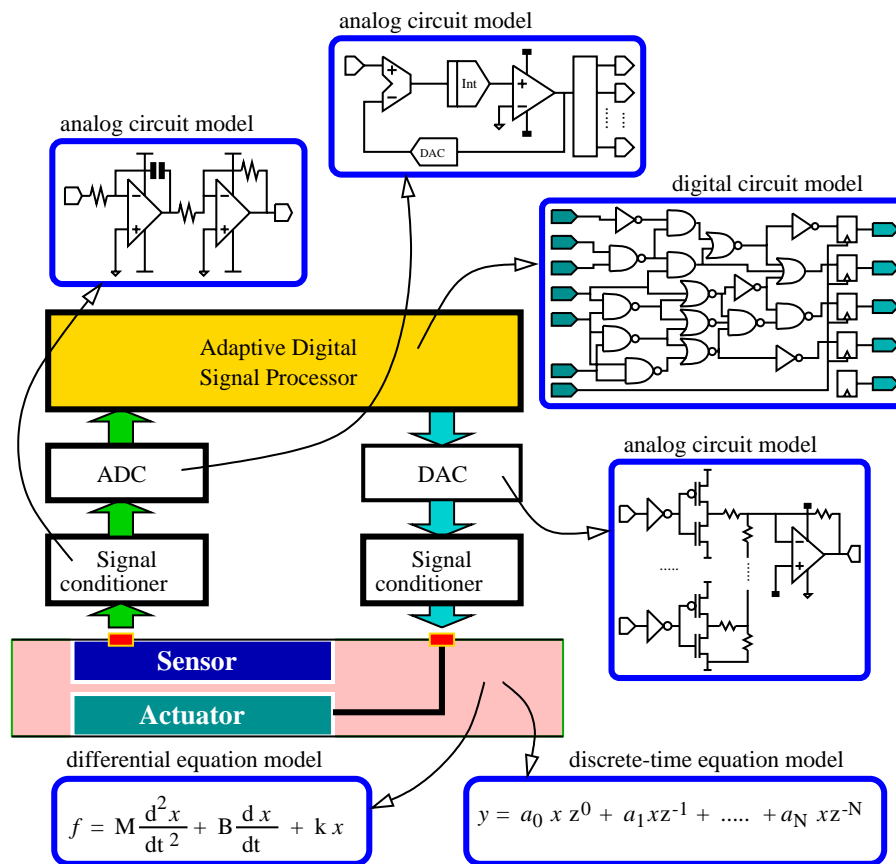




Studien-, Bachelor-, Master-, Diplomarbeit



Topic

Modelling and Simulation of heterogeneous subsystems in adaptronic applications.

Description

Heterogeneous subsystems such as Laplace and Z-transfer functions, ADC, Low-Pass Filter, Digital Circuit Core, DAC, Sample-Hold circuit, Power Amplifier circuit, etc will be modelled by using VHDL-AMS (Analog/Mixed-Signal). VHDL-AMS is an advanced hardware description language standardized by IEEE, which can be used to model heterogeneous systems such as difference and differential equations, linear and non linear system models, analog and digital circuits, etc. Each subsystem will be independently modelled as a modular cell or component. The interactions between the components will be simulated and observed by using AMS Designer tools from Cadence. In this topic, Student who has interest will gain an advanced skill to model heterogeneous systems and to simulate mixed analog-digital systems.

Skills

- Familiar with Cadence CAD tools.
- Knowledge of VHDL or Verilog HDL.

Contact

If you have interest in the topic please feel free to contact us.



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