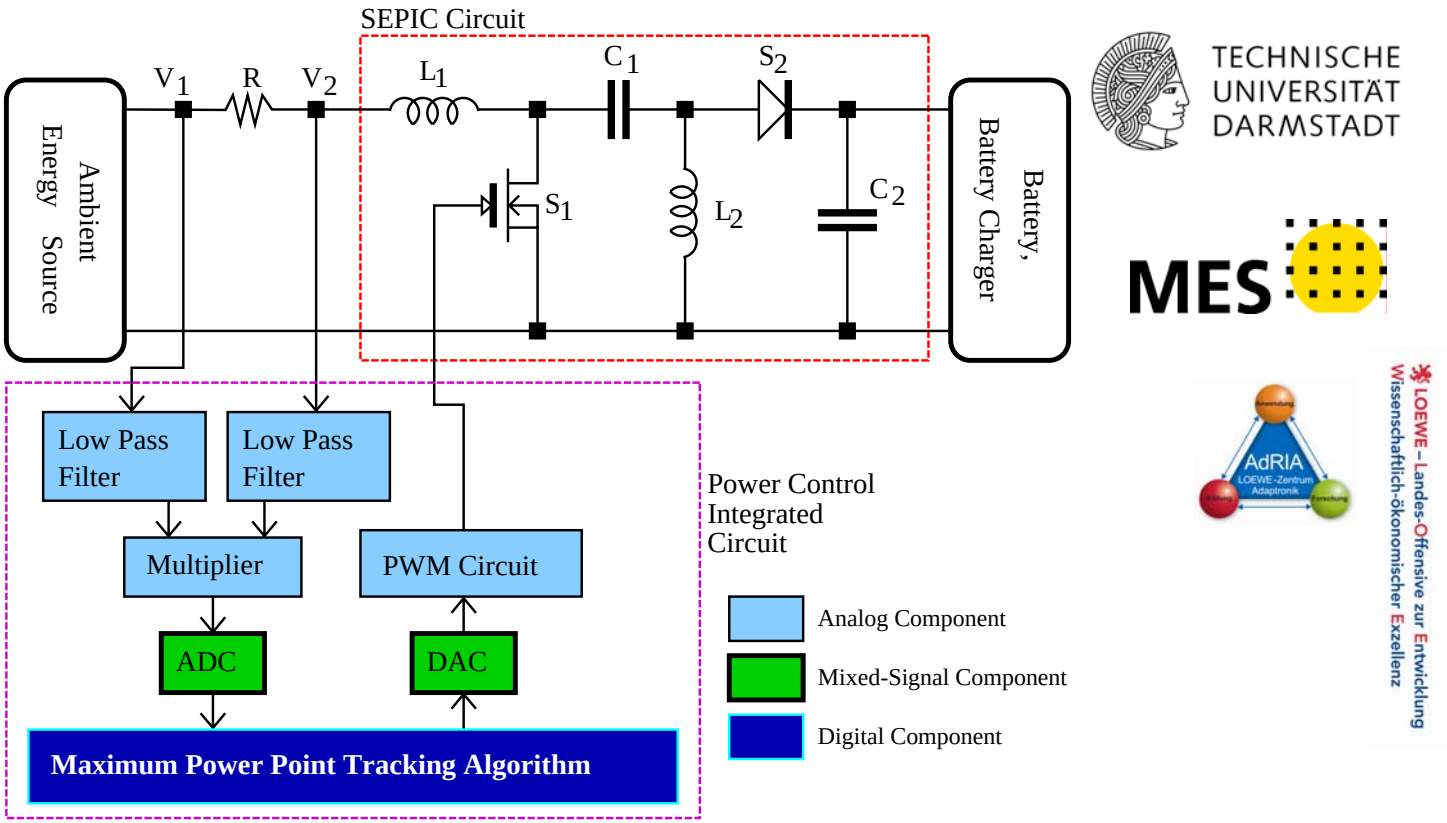

TECHNISCHE UNIVERSITÄT DARMSTADT

MES

AdRIA LOEWE-Zentrum Adaptronik

LOEWE – Landes-Offensive zur Entwicklung Wissenschaftlich-ökonomischer Exzellenz

Studien-, Bachelor-, Master-, Diplomarbeit

Topic 1: IP Core design for a CMOS analog multiplier circuit.
Topic 2: IP Core design for a CMOS analog subtractor and low pass filter.
Topic 3: IP Core design for a CMOS analog-to-digital converter (ADC) circuit.

Description

In the frame of the Project LOEWE-AdRIA (Adaptronik-Research, Innovation, Application), we are offering a Bachelor/Master Thesis or (Studien-/Diplomarbeit) about IP core design of an analog multiplier, subtractor and low pass filter, which are used in an ambient energy harvesting system. The cores will be integrated together with a PWM-Circuit for a solar-based and/or a TEG-based energy-harvesting system, which is an important part for battery-powered wireless sensor network applications.

Skills

- Experience with Cadence Tools (schematic and circuit layout)
- Knowledge of analog circuit design

Contact

If you have interest in the topic please feel free to contact me.

The Art of Adapting

Dr.-Ing. Faizal Arya Samman
S3|06 352a, Merckstrasse 25, 64283 Darmstadt
faizal.samman@mes.tu-darmstadt.de,
faizal.samman@loewe-adria.de