

8th International Workshop on Reconfigurable Communication-centric Systems-on-Chip



July 10 - 12 2013, Darmstadt, Germany



www.recosoc.org

Call For Papers

General Chair

Manfred Glesner,
TU Darmstadt (DE)

General Co-Chair

Gilles Sassatelli
LIRMM (FR)

Program Co-Chairs

Alberto Garcia-Ortiz,
Universität Bremen (DE)

Gert Jervan,
Tallinn University of
Technology (EE)

Program Committee

See www.recosoc.org

Local Organization

François Philipp
TU Darmstadt (DE)

Contact

contact@recosoc.org
MES Research Group
Merckstr. 25
64283 Darmstadt
Germany

During the past 7 years, **ReCoSoC** has established itself as a reference international event for researchers in the areas of reconfigurable and communication-centric systems-on-chip. Its informal and dynamic philosophy encourages the technical and scientific interactions between senior academics and young researchers.

All accepted papers will be published in **IEEE Xplore**, and the authors of the best papers will be invited to submit an extended version of their contribution in ACM Transactions on Reconfigurable Technology and Systems (**ACM TRET**S)

Topics of interest :

The areas of interest include (but are not limited to) :

- New paradigms for reconfigurable and communication-centric computing
- Reconfigurable and adaptive embedded SoCs
- Communication-centric design techniques at different abstraction levels
- On-chip communication architectures
- Low power design of reconfigurable and multiprocessor SoCs
- Communication-aware multiprocessor embedded systems
- OS and middleware for reconfigurable and multicore SoCs
- Specification languages and design methodologies
- Verification and evaluation techniques
- Industrial case studies
- (special sessions)

Submission :

Authors are invited to submit contributions as maximum 8 page papers in IEEE conference format. Contribution(s) have to be submitted electronically through the EasyChair portal of the conference :

<https://www.easychair.org/conferences/?conf=recosoc2013>

Important Dates :

Submission deadline regular papers : 26th April 2013–10th May 2013 (extended)

Submission deadline special sessions : 26th April 2013–17th May 2013 (extended)

Notification of acceptance : 31st May 2013

Camera-ready due : 14th June 2013



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Architecture, programming and run-time techniques for heterogeneous MPSoCs

Organization : Luciano Ost, LIRMM (FR)

- Efficient mapping/scheduling
- Programmability of Heterogeneous MPSoC
- Virtualization techniques and platform clustering
- Multi-domain development methodology
- Power-efficient for Heterogeneous Platform

Reconfigurable Systems for Security Applications

Organization : Marc Stöttinger, Nanyang Technological University (SG)

- Efficient and High Performance Implementation for FPGA-based Crypto Systems
- IP-core Rights Management and Bitstream Encryption
- Side Channel and Fault Attack Resistant FPGA Implementations
- Design Flow for FPGA-based Secure Systems
- Hardware Trojan Design and Detection on Reconfigurable Systems

Architecture and development for application specific processors

Organization : Michael Hübner, Uni Bochum (DE)

- Architecture Description Languages
- Compiler
- Processor Microarchitecture
- Instruction Set Definition

Algorithms for online-synthesis

Organization : Christian Hochberger and Andreas Koch, TU Darmstadt (DE)

- Low complexity algorithms for demand driven online-synthesis

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