

EWME 2010 – Preliminary Programme

DAY 1 (Monday, 10 May 2010)	
8:00	Registration
9:00	Welcome Address Manfred Glesner (<i>Technische Universität Darmstadt, General Chair</i>), Helmut F. Schlaak (<i>Dean of the Department of Electrical Engineering and Information Technology, Technische Universität Darmstadt</i>), Reiner Anderl (<i>Vice President, Technische Universität Darmstadt</i>)
9:20	Keynote Paolo Lugli (<i>Technische Universität München</i>), " Teaching Nanoelectronics "
10:00	Coffee break
10:20	Keynote Carl Das (<i>IMEC</i>), John McLean (<i>STFC</i>), " EUROCHIP-EUROPRACTICE – 20 Years of Design Support for European Universities "
11:20	Keynote Bernard Courtois (<i>CMP</i>), " And now, where do we go? "
12:00	Lunch
13:40	Christoph Kutter (<i>Infineon</i>), " Challenges in the Development of Mobile Phones " (Invited Talk)
14:20	Tor Lund-Larsen (<i>Intel</i>), " Single chip cloud computing " (Invited Talk)
15:00	Session I "Education with Industry Needs in Mind" (Session Chair: Daniel Donoval) Matthias Hanke , Thomas Schuster, Tim Kranich, Mladen Berekovic (<i>Technische Universität Braunschweig</i>), Ralf Götsche (<i>Intel</i>): Teaching VLSI Design Considering Future Industrial Requirements Béatrice Pradarelli , Laurent Latorre, Marie-Lise Flottes, Yves Bertrand, Pascal Nouet (<i>University Montpellier II</i>): Industrial Testing Education at Undergraduate Level – A Datasheet and Diagnosis Based Labs Approach

15:40	<p>Poster Session</p> <p>Gilles Jacquemod (<i>University of Nice – Sophia antipolis</i>), Jacques-Olivier Piednoir (<i>Cadence Design Systems</i>): SAME goes back to school</p> <p>Juan Jesus Oampo-Hidalgo (<i>Universidad Autonoma Metropolitana</i>), Jesus E. Molinar-Solis (<i>Universidad del Estado de Mexico</i>), Andres Ferreyra-Ramirez (<i>Universidad Autonoma Metropolitana</i>): Teaching Analog Design Concepts in Deep Submicrometric Technologies</p> <p>Binyamin Abramov (<i>TAU, Israel</i>), Osnat Keren (<i>BIU, Israel</i>), Ilya Levin (<i>TAU, Israel</i>): Teaching Cognitive-Inspired Design of Sequential Circuits</p> <p>Gerhard Gruhler (<i>Reutlingen University</i>), Jörg Schulze (<i>University of Stuttgart</i>), Markus Lang (<i>Robert Bosch GmbH</i>): A New Joint Research and Teaching Project on Micro-and Power Electronics</p>	Coffee break
16:40	<p>Session II "E-Learning" (Session Chair: Michel Declercq)</p> <p>Raimund Ubar, Artur Jutman, Jaan Raik, Sergei Devadze, Maksim Jenihhin, Igor Aleksejev, Sergei Kostin, Anton Chertov, Anton Chepurov, Elmet Orasson (<i>Tallinn University of Technology</i>), Heinz-Dietrich Wuttke (<i>Technische Universität Ilmenau</i>): E-Learning Environment for WEB-Based Study of Testing</p> <p>Belgacem Hamdi (<i>ISSAT Sousse</i>), Etienne Sicard (<i>INSA Toulouse</i>): Introduction to Fault Analysis at Logic Level – an Educational Approach based on DSCH</p> <p>Lubica Stuchlíková, Jana Benkovská, Ján Šebok (<i>Slovak University of Technology</i>): Interactive eLearning Courses</p> <p>Ján Šebok, Lubica Stuchlíková, Michal Nemeč, Jana Benkovská, Ladislav Harmatha, Daniel Donoval (<i>Slovak University of Technology</i>): eLearning as a Support in Education of Young Experimenters</p>	
18:00	<p>Dinner Speech</p> <p>Mark Harris (<i>Intel</i>), "Intel's Higher Education Strategy – Accelerating Innovation Globally"</p>	Reception

DAY 2 (Tuesday, 11 May 2010)

9:00	<p>Michel Declercq (<i>EPFL Lausanne</i>), Herman Maes (<i>IMEC</i>): EURODOTS – A New EC Support Action for Doctoral Training in Europe</p>
9:20	<p>Session III "Teaching Nano-Scale Design" (Session Chair: Kunihiro Asada)</p> <p>Wieslaw Kuzmicz (<i>Warsaw University of Technology</i>), Bart DeMey (<i>IMEC</i>): Old IDESA and New IDESA-2: European Training Programs for Implementation of DSM CMOS ASICs</p> <p>Etienne Sicard, Sonia Ben Dhia (<i>INSA Toulouse</i>), Syed Mahfuzul Aziz (<i>University of South Australia, Mawson Lakes</i>): Teaching CMOS Circuit Design in Nanoscale Technologies Using Microwind</p> <p>Jerry Gulley (<i>Strategic Security Alliance</i>), Friedrich Holl (<i>Brandenburg University of Applied Sciences</i>), Thaddeus P. Kochanski (<i>IEEE-Boston</i>), Andrzej Rucinski (<i>University of New Hampshire</i>): A Pilot Course in Vital Electronics</p> <p>Ivan Ring Nielsen (<i>Technoconsult ApS</i>), Erik Bruun (<i>Technical University of Denmark</i>): EuroTraining – Supporting University Programms in Nanoelectronics</p>
10:40	Coffee break
11:00	<p>Session IV "Teaching System Design" (Session Chair : Peeter Ellervee)</p> <p>Vadim Pesonen, Maksim Gorev, Kalle Tammemäe (<i>Tallinn University of Technology</i>): Learning-by-Gaming in HW/SW Codesign</p> <p>Tim Kranich, Thomas Schuster, Matthias Hanke, Mladen Berekovic (<i>Technische Universität Braunschweig</i>), Ralf Göttsche (<i>Intel</i>): Teaching the Principles of System Design, Platform Development and Hardware Acceleration</p> <p>Ralf Salomon, Ralf Joost, Wiebke Schwelgenraber (<i>University of Rostock</i>): The Model Railroad as an Inspiring Platform for Microelectronics Education</p>
12:00	Lunch
13:40	<p>Hannu Tenhunen (<i>University of Turku</i>), "EIT ICT Labs – Empowering the Engineering Education" (Invited Talk)</p>
14:20	<p>Ricardo Reis (<i>Universidade Federal do Rio Grande do Sul</i>), "Microelectronics Education in the South of Brazil and Outcomes" (Invited Talk)</p>

15:00	Jakub Rybár , Lubica Stuchlíková, Jaroslav Kováč jr., Jaroslav Kováč (<i>Slovak University of Technology</i>): Multimedia in Optoelectronics and Sensorics	
15:20	Special Session "Microelectronics Education at German Universities of Applied Sciences" (Session Chair: Manfred Glesner)	
	Dirk Jansen (<i>University of Applied Sciences Offenburg</i>): Microelectronic Education by Doing - the MPC-Group of Baden-Württemberg	
15:40	Poster-Session Jeremie Hamon (<i>PHELMA Grenoble Institute of Technology</i>), Vincent Fristot (<i>PHELMA Grenoble Institute of Technology</i>), Robin Rolland (<i>CIME-Nanotech</i>): FPGA implementation of a real time multi-resolution edge detection video filter Moritz Prinz, Sebastian Vagedes, Kristian Wessels, Peter Glösekötter (<i>Muenster University of Applied Science</i>), Edmund Coersmeier (<i>Task9 GmbH</i>), Josef Decking (<i>Muenster University of Applied Science</i>): Export Mobile Device Display Content Vazgen Melikyan (<i>Synopsys Armenia CJSC</i>), Rich Goldman (<i>Synopsys Inc.</i>), Karen Bartleson (<i>Synopsys Inc.</i>), Troy Wood (<i>Synopsys Inc.</i>): Synopsys' Interoperable Process Design Kit Bernhard Hoppe (<i>University of Applied Sciences Darmstadt</i>): Integrating Microelectronics Into A Distant Learning Course	Coffee break
16:20	Panel "What can industry provide for education beyond licenses?" Mladen Berekovic (<i>Technische Universität Braunschweig</i>) Gert Jervan (<i>Tallinn University of Technology</i>) Michel Declercq (<i>EPFL Lausanne</i>) Sandra Larrabee (<i>EVE USA</i>) Patrick Haspel (<i>Cadence Design Systems</i>) Menno Lindwer (<i>Silicon Hive</i>) Steven Redant (<i>IMEC</i>)	
18:00	Dinner Speech Markus Olbrich (<i>Leibniz-Universität Hannover</i>), "VDE / BMBF Initiative Invent a Chip" Regina Hartfiel (<i>Justus-Liebig-Gymnasium, Darmstadt</i>), "Der Anti-Anbrenn-Chip im Rahmen von Invent-A-Chip"	Dinner

DAY 3 (Wednesday, 12 May 2010)

8:40	Maurits Ortmanns (<i>Ulm University</i>), "Teaching Analog CMOS in the DSM era - Devices, Designs and Tools" (Invited Talk)
9:20	Keynote Andrei Vladimirescu (<i>Institut Supérieur d'Electronique de Paris & UC Berkeley</i>), "SPICE - From Student Project, to Learning Tool, to Industry Standard for IC Design"
10:00	Coffee break
10:20	Session V "Teaching Hardware Description Languages" (Session Chair Peter Zipf) Abdulahdi Shoufan , Sorin Alexander Huss (<i>Technische Universität Darmstadt</i>): Analog and Mixed-Signal Modeling with VHDL-AMS Zheng Lu , Abdulahdi Shoufan, Guido Röbling (<i>Technische Universität Darmstadt</i>): A Platform for VHDL Visualization
11:00	Session VI "Course Programs" (Session Chair: Marta Rencz) Kjell Jeppson , Lena Peterson, Lars Svensson, Per Larsson-Edefors (<i>Chalmers University of Technology</i>): A CDIO-Oriented Master's Program in Integrated Electronic System Design Tobias Schwalb , Adnene Gharbi, Klaus D. Müller-Glaser, Marianne Aders , Anke Diez (<i>Karlsruhe Institute of Technology</i>): Initiation of a Course Package based on the Bologna process with Support of the Department of Educational Development
11:40	Lunch
13:00	Keynote Eugenio Cantatore (<i>Eindhoven University of Technology</i>), "Organic Electronics – Technology, Devices, Circuits and Applications"
14:00	Irina Munteanu (<i>Technische Universität Darmstadt</i>), "Maxwell meets Kirchhoff – From Circuits Towards Fields" (Invited Talk)
14:20	Session VII "Teaching Electromagnetic Field Dynamics" (Session Chair: Irina Munteanu)

	<p>Ulrich Rohde (<i>Synergy Microwave Inc.</i>), Hans Hartnagel (<i>Technische Universität Darmstadt</i>): The Dangers of a Simple Usage of Microwave Software</p> <p>Etienne Sicard, Alexandre Boyer (<i>INSA Toulouse</i>): An Educational Approach to Electromagnetic Compatibility of Integrated Circuits</p>
15:00	Coffee break
15:20	<p>Dimitris Pavlidis (<i>Technische Universität Darmstadt</i>), "DUDECRONO – a EU-US dual-degree master on micro and nano technology" (Invited talk)</p>
15:40	<p>Session VIII "Inspiring Highschool and First-Semester Students" (Session Chair: Dante Del Corso)</p> <p>Cora Salm, Jan Eikel, Erik Faber, Ferdi van der Heijden, Mathieu Odijk (<i>University of Twente</i>): Adapting to a Changing Highschool Population</p> <p>Maksim Gorev, Vadim Pesonen, Peter Ellervee (<i>Tallinn University of Technology</i>): Introducing Computer Systems Related Topics in the First Study Semester</p>
16:20	<p>Closing Remarks</p> <p>Bernard Courtois (<i>CMP</i>) Manfred Glesner (<i>TU Darmstadt</i>)</p>